Serial No. 09/330,743

<u>REMARKS</u>

This Amendment with a Request for Continued Examination is filed to prosecute amended claims 11-26. In particular claims are amended to state with more specificity that the predefined bit patterns are received from the serial transmission channel. Misalignment caused by differences in transmission channels are reflected in the predefined bit patterns associated with each group of parallel data bits. By determining offset between predefined bit patterns associated with different groups and adjusting the respective predefined bit patterns to remove the offset therebetween the misalignment is corrected.
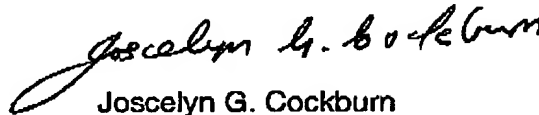
There is no suggestion or motivation in the references to make the combination as recited in the amended claims. Without motivation set forth in one of the references and/or the Examiner set forth logical and concrete reason the combination would have been improper. It is applicants' contention none of these conditions exist. Therefore, the claims 11-26 as amended are not obvious over the art of record.

In addition, the details of circuit arrangements set forth in claims 14, 15, 16, 17, 18 and 19 through 24 are not suggested in the references. Because the circuit arrangements are not found in the prior art, they are novel. In addition, the circuits are relatively easy to implement. The ease of implementation is a benefit to users. It is applicants' contention the novel structure coupled with benefits are indicia of nonobviousness. Therefore, this group of claims is patentable over the art of record.

-7-

Serial No. 09/330,743   IBM Docket No. RA998-040

It is believed the present RCE overcomes the rejection based upon references set forth in the Final Office Action. Reconsideration is hereby requested and an early allowance of the claims is solicited.

Respectfully Submitted,

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- 8 -